

INFORMATION DISCLOSURE STATEMENT
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First Named Inventor	Rajski
Art Unit	2133
Examiner Name	Phung M. Chung

U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

Examiner's Initials*	Cite No. (optional)	Country	Number	Publication Date	Name of Applicant or Patentee

OTHER DOCUMENTS

Examiner's Initials*	Cite No. (optional)	
		Chakrabarty et al., "Optimal Space Compaction of Test Responses," <i>Proc. ITC</i> , pp. 834-843 (1995).
		Chakrabarty et al., "Test Response Compaction Using Multiplexed Parity Trees," <i>IEEE Trans. CAD</i> , Vol. CAD-15, No. 11, pp. 1399-1408 (1996).
		Ivanov et al., "Programmable BIST Space Compactors," <i>IEEE Trans. Computers</i> , Vol. 45, No. 12, pp. 1393-1404 (1996).
		Pouya et al., "Synthesis of Zero-Aliasing Elementary-Tree Space Compactors," <i>Proc. VLSI Test Symp.</i> , pp. 70-77 (1998).
		Reddy et al., "A Data Compression Technique for Built-In Self-Test," <i>IEEE Trans. Computers</i> , Vol. 37, No. 9, pp. 1151-1156 (1988).
		Wunderlich, "Multiple Distributions for Biased Random Test Patterns," <i>Proc. ITC</i> , pp. 236-244 (1988).
		Yamaguchi et al., "An Efficient Method for Compressing Test Data," <i>Proc. ITC</i> , pp. 79-88 (1997).

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